

Physical: Letter

On the origin of HOLZ lines splitting near interfaces: multislice simulation of CBED patterns

Andrey Chuvilin^{1,*}, Ute Kaiser¹, Quentin de Robillard² and Hans-Jürgen Engelmann²

¹University of Ulm, Albert Einstein Allee 11, D-89069 Ulm, Germany and ²AMD Saxony LLC & Co. KG, PO Box 110110, D-01330 Dresden, Germany

*To whom correspondence should be addressed. E-mail: andrey.chuvilin@uni-ulm.de

Abstract Splitting of HOLZ lines on CBED patterns is systematically observed at the proximity of interfaces and prevents local strain measurements by monitoring of line shifts. It was previously suggested that such splitting occurs due to interface-strain relaxation in thin TEM lamella. Here we confirm this model by dynamical simulation of CBED patterns using the multislice algorithm.

Keywords CBED, strain analysis, line splitting, dynamical simulation, multislice

Received 4 March 2005, accepted 12 December 2005, online 13 January 2006

Recent developments in semiconductor devices showed that CMOS transistor performance could be considerably improved by introducing mechanical stress in silicon [1,2]. To develop and stabilize finite element method calculation, and finally to optimize the transistor performance, it is necessary to have a technique capable to measure the strain distribution in the transistor channel region with an accuracy below 1% and with nanometer resolution.

One of the most promising techniques is the Convergent Beam Electron Diffraction (CBED) method [3–7], because higher order Laue zone (HOLZ) line positions shift sensitively with lattice parameter changes. Recently developed

software [8] provides the possibility to extract the strain tensor semi-automatically from the CBED pattern of silicon and thus opens this generally very time-consuming method to the industry. Based on a line detection algorithm and the kinematical theory, the software utilizes the $\langle 230 \rangle$ Si zone at 200 kV. A reference pattern is taken deep in the Si in order to observe a relative shift of HOLZ lines in the region of interest. But as seen in Fig. 1, while the pattern obtained at 500 nm depth below the interface corresponds well to the expected $\langle 230 \rangle$ Si CBED pattern and represents sharp lines, in the region closer to the interface, i.e. in the region defining the transistor properties, broadening and

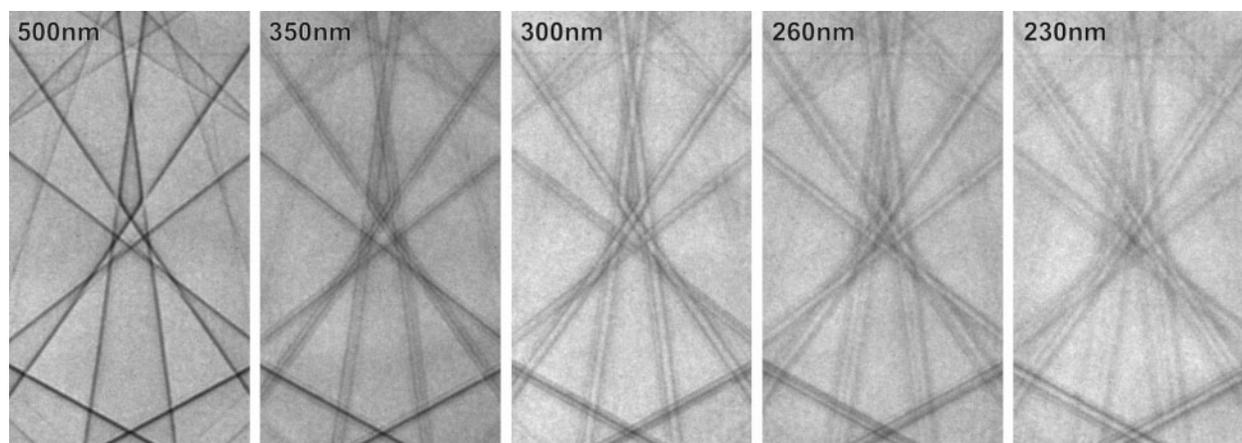


Fig. 1 A stack of Si $\langle 230 \rangle$ CBED patterns, obtained at the positions sequentially approaching the interface (left to right, distance to interface is indicated), shows the evolution of the HOLZ lines splitting.

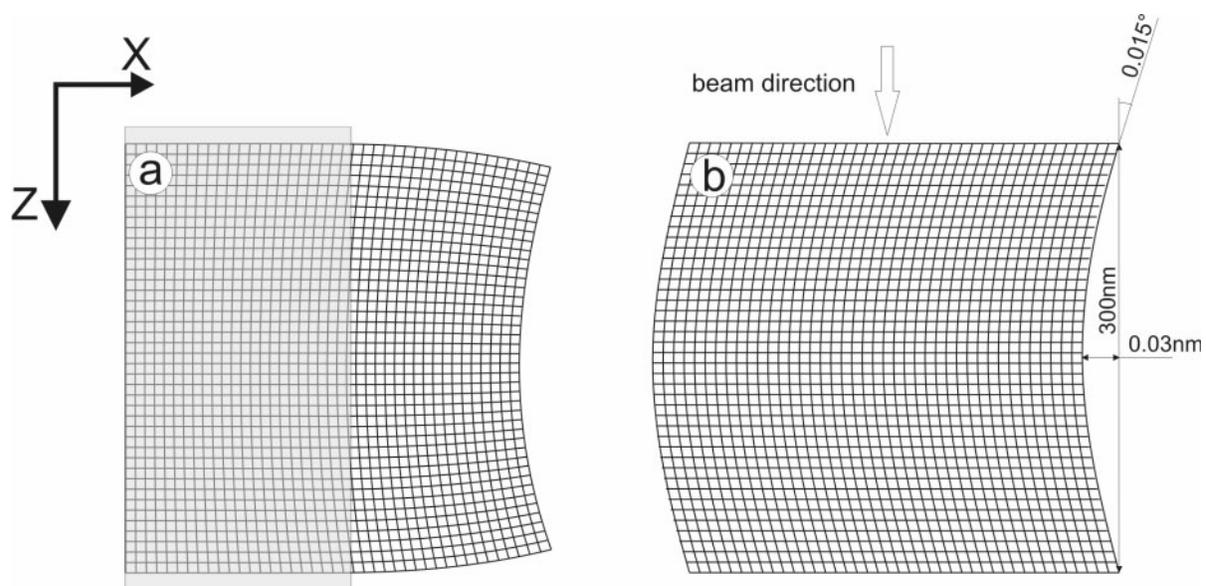


Fig. 2 (a) Scheme of a strain field near an interface after thin lamella relaxation; (b) scheme of a strain field utilized in the simulation. The $\partial e_{zz}/\partial x$ component of strain gradient is excluded; actual values of applied displacements are indicated. The shaded rectangle at (a) indicates the region where the $\partial e_{zz}/\partial x$ component of strain gradient is negligible, so that model (b) is correct for this region.

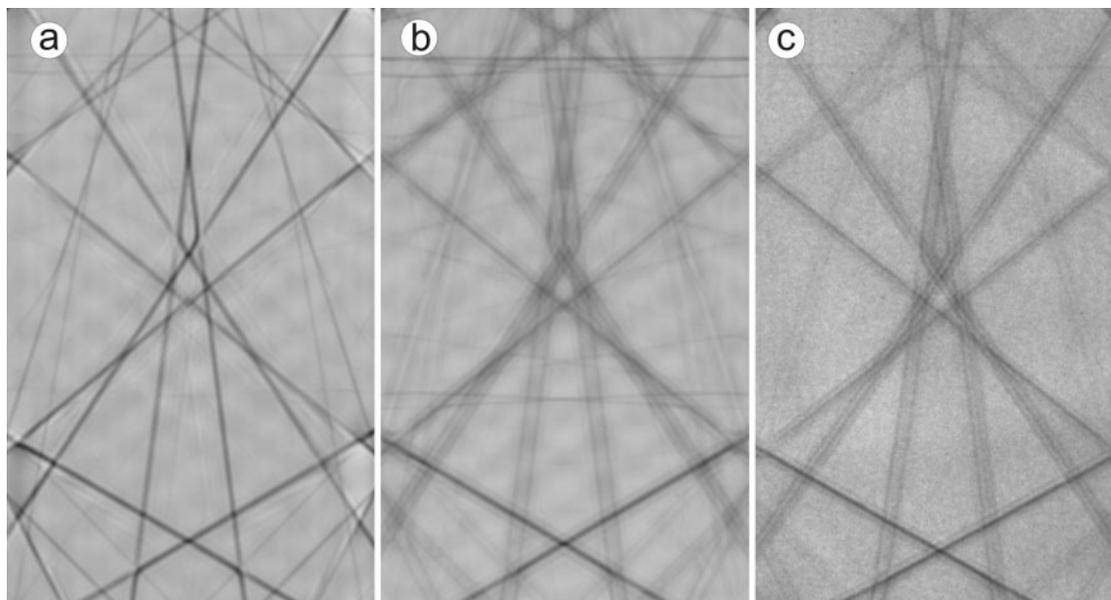


Fig. 3 Central parts of the 000 CBED disks for Si $\langle 230 \rangle$ zone at 200 kV: (a) simulated pattern for a perfect crystal; (b) simulated pattern for bended crystal in accordance to Fig. 2b; (c) experimental image (identical to Fig. 1, 350 nm) at similar conditions (high tension deviates slightly from the nominal 200 kV, 300 nm thickness, measured by thickness fringes of the CBED pattern).

splitting-like effects are observed, which invalidate the usage of line-shift based detection algorithm.

Observing that the splitting effect did not appear near a free Si but near a covered Si surface, Banhart [9] explained the splitting by lattice bending caused by strain relaxation in the thin TEM lamella. The strain originated from the thermal expansion coefficient difference between Si and the cover layer. Later this model was discussed by a number of authors [10,11]; however, no proof has been provided so far. Here

we apply the multislice algorithm in order to simulate the HOLZ lines splitting effect in a bended crystal.

The multislice method is widely and successfully used for the calculation of scattering of fast electrons for the purpose of HRTEM image simulation. Recently we demonstrated that this method is also capable of calculating HOLZ lines in CBED patterns [12,13]. The advantage of the multislice method is that it does not require an approximation of slowly varying crystal parameters and thus it is a perfect tool

for simulation of electron scattering by strained materials. So far the existing algorithm does not allow simulation of any strain field with the same efficiency: simulation of strain fields involving $\partial e_{zz}/\partial x$ and $\partial e_{zz}/\partial y$ strain gradient components would require enormous time.

Therefore we simplified the model suggested in [9–11] by excluding the $\partial e_{zz}/\partial x$ component (see Fig. 2). The strain field without the $\partial e_{zz}/\partial x$ gradient component can be expected at some distance away from the interface. A model of a silicon crystal in $\langle 230 \rangle$ orientation with the dimensions of $55 \times 55 \times 300 \text{ nm}^3$ was built. Bending was applied in the X ($\langle 001 \rangle$) direction with the maximum shift of 0.03 nm at the mean thickness, which corresponded to $\sim 0.015^\circ$ maximum distortion at the entrance and exit surfaces.

CBED patterns were simulated for 200 kV accelerating voltage and 20 mrad convergence angle. Figure 3 shows simulated central CBED disks for the undisturbed (a) and bended (b) crystals, the experimental image (c) is represented for comparison. It is seen that the model describes semi-quantitatively different types of line splitting: in two and three components. Slight divergence in line positions should be attributed to the deviation of experimental high tension from the nominal 200 kV. We suppose that accounting for the $\partial e_{zz}/\partial x$ strain gradient component is necessary to explain the more complex splitting and broadening features, when further approaching the strained interface (see Fig. 1).

Thus we demonstrated by simulation the validity of the earlier suggested model for the explanation of HOLZ line splitting near the interface. Splitting occurs due to the bending of the crystal near the interface because of the strain relaxation in thin TEM lamella during sample preparation. Further we are going to quantify the effect. Quantification is necessary for the reconstruction of complete strain field in the TEM lamella, which in turn will allow the reconstruction of the strain state in the original bulk sample.

References

- 1 Keunwoo K, Joshi R V, and Chuang C (2003) Strained-Si devices and circuits for low-power applications. In: *Proceedings of the 2003 International Symposium on Low Power Electronics and Design, ISLPED 03, 25–27 August 2003*, pp. 180–183.
- 2 Hoy J L, Nayfeh H M, Eguchi S, Aberg I, Xia G, Drake T, Fitzgerald E A, and Antoniadis D A (2002) Strained silicon MOSFET technology. In: *Electron Devices Meeting 2002, IEDM 02, 8–11 December 2002*, pp. 23–26.
- 3 Carpenter R W and Spence J C N (1982) Three-dimensional strain-field information in convergent-beam electron diffraction patterns. *Acta Crystallogr.* **A38**: 55–61.
- 4 Zuo J M, Kim M, and Holmestad R J (1998) A new approach to lattice parameter measurements using dynamical electron diffraction and pattern matching. *J. Electron Microsc.* **47**: 121–127.
- 5 Krämer S, Mayer J, Witt C, Weickenmeier A, and Rühle M (2000) Analysis of local strain in aluminium interconnects by energy filtered CBED. *Ultramicroscopy* **81**: 245–262.
- 6 Kaiser U, Saitoh K, Tsuda K, and Tanaka M (1999) Application of the CBED method for the determination of lattice parameters of cubic SiC films on 6H SiC substrates. *J. Electron Microsc.* **48**: 221–233.
- 7 Chuvilin A, Kups Th, and Kaiser U (2004) The effect of the signal-to-noise-ratio in CBED patterns on the accuracy of lattice parameter determination. *J. Electron Microsc.* **53**: 237–244.
- 8 Armigliato A, Balboni R, Carnevale G P, Pavia G, Piccolo D, Frabboni S, Benedetti A, and Cullis A G (2003) Application of convergent beam electron diffraction to two-dimensional strain mapping in silicon devices. *Appl. Phys. Lett.* **82**: 2172–2174.
- 9 Banhart F (1994) Strains in crystals with amorphous surface films studied by convergent beam electron diffraction and high-resolution imaging. *Ultramicroscopy* **56**: 233–240.
- 10 Kraemer S, Mayer J, Witt C, Weickenmeier A, and Rühle M (2000) Analysis of local strain in aluminium interconnects by energy filtered CBED. *Ultramicroscopy* **81**: 245–262.
- 11 Pantel R, Clement L, Rouviere J L, and Kwakman L F Tz (2003) Strain measurements at a NiSi/Si interface using STEM-CBED: a quantification method for stress relaxation during TEM lamella preparation. *Microsc. Microanal.* **9**[Suppl. 2]: 866–867.
- 12 Chuvilin A and Kaiser U (2004) CBED of strained materials. In: *Proceedings of 13th European Microscopy Congress, EMC 2004, 22–27 August 2004*, Antwerp, Belgium, Vol. 1, pp 143–144.
- 13 Chuvilin A and Kaiser U (2005) On the peculiarities of CBED pattern formation revealed by multislice simulation. *Ultramicroscopy* **104**: 73–82.